

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Original) A plasma display panel, comprising:
  - a first substrate;
  - a plurality of first substrate electrode pairs formed on the first substrate in a first direction;
  - a second substrate;
  - a plurality of second substrate electrodes formed on the second substrate in a second direction crossing the first substrate electrode pairs in the first direction;
  - a dielectric layer formed on the second substrate and the second substrate electrodes;
  - barriers formed on the dielectric layer aligned between the second substrate electrodes;
  - a phosphor layer formed on the dielectric layer including the barriers; and
  - a plurality of projections formed on the phosphor layer between the barriers.
2. (Original) The plasma display panel of claim 1, wherein the plurality of projections are separated from each of the plurality of barriers at the lower portion, the upper portion, or the lower and upper portions of each barrier.

3. (Original) The plasma display panel of claim 1, wherein at least one of the plurality of projections is tapered.

4. (Original) The plasma display panel of claim 1, wherein at least one of the plurality of projections has a conical shape.

5. (Original) The plasma display panel of claim 1, wherein at least one of the plurality of projections has a polygonal shape.

6. (Original) The plasma display panel of claim 1, wherein at least one of the plurality of projections has a height equivalent to the height of at least one of the barriers.

7. (Canceled)

8. (Currently Amended) ~~The plasma display panel of claim 7, further comprising: A~~  
plasma display panel, comprising:

a first substrate;

a plurality of first substrate electrode pairs formed on the first substrate in a first  
direction;

a first dielectric layer formed on the first substrate and the first substrate electrode  
pairs;

a second substrate;

a plurality of second substrate electrodes formed on the second substrate in a second direction to cross the first substrate electrode pairs in the first direction;

a second dielectric layer formed on the second substrate and the second substrate electrodes;

barriers formed on the second dielectric layer, wherein an exhaust path is formed between the first and second dielectric layers;

a phosphor layer formed on layer and the barriers; and

a plurality of projections formed on the phosphor layer between the barriers, wherein the exhaust path is between the barriers, the plurality of projections and the first and second dielectric layers.

9. (Original) The plasma display panel of claim 8, wherein the plurality of projections are separated from the barriers by the exhaust path.

Claims 10-12. (Canceled)